

#4

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Atty. Docket No. 010321	Serial No. RECEIVED MAR 14 2002 Technology Center 2100
	Applicant Robert H. Thibadeau	
	Filing Date 7/25/01	Group 2100



U. S. PATENT DOCUMENTS

Examiner Initial	Document Number	Issue Date	Patentee	Class	Sub-Class	Filing Date
PP	5,027,401	Jun. 25, 1991	Soltesz			
PP	5,101,374	Mar. 31, 1992	Sinutko, Jr.			
PP	5,394,469	Feb. 28, 1995	Nagel et al.			
PP	6,044,349	Mar. 28, 2000	Tolopka et al.			
PP	6,092,202	Jul. 18, 2000	Veil et al.			
PP	6,138,239	Oct. 24, 2000	Veil			
PP	6,141,752	Oct. 31, 2000	Dancs et al.			
PP	6,175,924 B1	Jan. 16, 2001	Arnold			
PP	6,182,222 B1	Jan. 30, 2001	Oparaji			
PP	6,192,472 B1	Feb. 20, 2001	Garay et al.			

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

Examiner Initial	Document Number	Public. Date	Country or Patent Office	Class	Sub-Class	Transl Y N

Examiner PRAMILA PARTHASARATHY	Date Considered July 16 - 2004
EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

#4

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Atty. Docket No. 010321	Serial No. RECEIVED
		MAR 14 2002 Technology Center 2100
	Applicant Robert H. Thibadeau	
	Filing Date 7/25/01	Group 2100



OTHER DOCUMENTS

(Including Author, Title, Date, Relevant Pages, Place of Publication)

		"TCPA PC Specific Implementation Specification," Version 1.00, September 9, 2001.
		Gobioff, H. et al., "Security for Network Attached Storage Devices," School of Computer Science, Carnegie Mellon University, October 23, 1997.
		Strunk, J. D. et al. "Self-Security Storage: Protecting Data in Compromised Systems," 4 th Symposium on Operating Systems Design and Implementation, October 2000.
		"Trusted Computing Platform Alliance (TCPA)," Main Specification Version 1.1a, November 12, 2001.
		"Intel® Protected Access Architecture," Application Interface Specification, Revision 1.0, March 2001.
Examiner PRAMILA PARTHASARATHY	Date Considered July - 16 - 2004	
EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		



FORM PTO-949	Atty. Docket No.: I69.12-0549	Application No.: 09/912,931
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	First Named Inventor: Robert H. Thibadeau	Group Art: 2131
	Filing Date: July 25, 2001	

RECEIVED

JAN 24 2003

Technology Center 2100

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
JP	AA	3,576,544	04/27/71	Cordero, Jr. et al.	340	172.5	10/18/68
JP	AB	3,890,601	06/17/75	Pietrolewicz	340	172.5	03/11/74
JP	AC	4,183,085	01/08/80	Roberts et al.	364	200	10/21/77
JP	AD	4,442,484	04/10/84	Childs, Jr. et al.	364	200	10/14/80
JP.	AE	5,022,077	06/04/91	Bealkowski et al.	380	4	08/25/89
JP	AF	5,345,590	09/06/94	Ault et al.	395	650	09/01/93
JP	AG	5,448,045	09/05/95	Clark	235	382	09/05/95
JP	AH	5,623,637	04/22/97	Jones et al.	395	491	05/17/96
JP	AI	5,754,821	05/19/98	Cripe et al.	395	491	03/27/96
JP	AJ	5,892,902	04/06/99	Clark	395	187.01	09/05/96
JP	AK	5,928,364	07/27/99	Yamamoto	713	202	11/27/96
JP	AL	5,949,601	09/07/99	Braithwaite et al.	360	60	06/27/97
JP	AM	6,134,662	10/17/00	Levy et al.	713	200	06/26/98
JP	AN	6,219,726	04/17/01	Ripberger	710	36	07/27/94
JP	AO	6,253,281	06/26/01	Hall	711	112	06/15/98
JP	AP	6,321,358	11/20/01	Anderson	714	763	08/27/98
JP	AQ	6,324,627	11/27/01	Kricheff et al.	711	163	06/02/99

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Class	Sub Class	Translation Yes No
JP	AR	JP5134863A2	06/01/93	Japan	G06F	9/06	Yes
JP	AS	JP8115266A2	05/07/96	Japan	G06F	12/14	Yes
	AT						



RECEIVED

JAN 24 2003

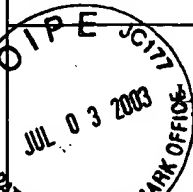
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Technology Center 2100

	AU	"Research on a Dedicated CPU Architecture for Java IC Card", Wang Tao, et al., ACTA ELECTRONICA SINICA, Vol. 28, No. 11, Nov. 2000, pp. 77-80, 76., Chinese Inst. Electron, China. <i>Abstract</i>
	AV	"Smart Cards With Built-in Microprocessors: Partition into Virtual Cards", R. Brenner, ELEKTRONIK, Vol. 43, No. 9, May 3, 1994, pp. 38-44, Germany. <i>Abstract</i>
	AW	"Public Protection of Software", ACM TRANSACTIONS ON COMPUTER SYSTEMS, Vol. 5, No. 4, November 1987, pp. 371-393.
	AX	"Protection and Control of Information Sharing in Multics", COMMUNICATIONS OF THE ACM, Vol. 17, No. 7, July 1974.
	AY	"Hardware Support for Memory Protection: Capability Implementations", M.V. Wilkes, Digital Equipment Corporation.
	AZ	"SECUR'ACCes: access control with the CP8 smart card", SECURICOM 87: 5th Worldwide Congress on Computer and Communications Security and Protection. SEDEP. 1987, pp. 187-96, Paris, France. <i>Abstract</i>
	BA	"Memory Protection Software Facility for OS/2 Shared Data Applications", IBM TECHNICAL DISCLOSURE BULLETIN, pp. 81-89, Sept. 1991.
	BB	"Modular System Partition", IBM TECHNICAL DISCLOSURE BULLETIN, pp. 341-342, Dec. 1992.
	BC	"Password Protection of Separate Hard Disk Partitions", IBM TECHNICAL DISCLOSURE BULLETIN, pp. 103-104, Nov. 1996.
	BD	"Preventing Access to a Personal Computer", IBM TECHNICAL DISCLOSURE BULLETIN, pp. 98-100, Sept. 1992.

EXAMINER: PRAMILA PARTHASARATHY DATE CONSIDERED: July 16 2004

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449		Atty. Docket No.: I69.12-0549	Application No.: 09/912,931
 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT		First Named Inventor: Robert H. Thibadeau	
		Filing Date: July 25, 2001	Group Art: 2131

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
JP	AA 6,446,209 B2	9/3/2002	Kern et al.			
JP	AB 6,336,187	1/1/2002	Kern et al.			
JP	AC 6,173,402 B1	1/9/2001	Chapman			
	AD					
	AE					
	AF					
	AG					

RECEIVED

JUL 10 2003

Technology Center 2100

FOREIGN PATENT DOCUMENTS

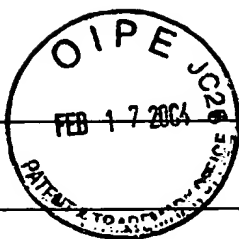
	Document No.	Date	Country	Class	Sub Class	Translation Yes No
AH						
AI						
AJ						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AK	
AL	
AM	

EXAMINER: PRAMILA PARTHASARATHY DATE CONSIDERED: July 16 2004

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



FORM PTO-1449	Atty. Docket No.: I69.12-0549	Application No.: 09/912,931
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	First Named Inventor: Robert H. Thibadeau	
	Filing Date: July 25, 2001	Group Art:

U.S. PATENT DOCUMENTS

Examiner Initials	Document No.	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents
AA			
AB			
AC			
AD			
AE			
AF			
AG			
AH			
AI			
AJ			
AK			

RECEIVED

FEB 18 2004

Technology Center 2100

FOREIGN PATENT DOCUMENTS

	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Translation Yes No
JP	AL EP 0 965 903 A1	12-22-1999	Colin Drew	
JP	AM WO 99/59049	11-18-1999	David Robb, et al.	
	AN			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AO	
AP	
AQ	

EXAMINER:

PRAMILA PARTHASARATHY

DATE CONSIDERED:

July 16 2004

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.